



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Harsh D. Sharma et al.

Title: PRIORITY DELAY INSERTION CIRCUIT

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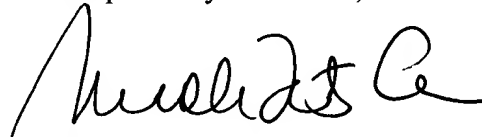
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Respectfully submitted,



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